

IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Trimberger  
Assignee: Xilinx, Inc.  
Title: System and Method for Securing Using Decryption Keys During  
FPGA Configuration Using a Microcontroller  
Serial No.: 10/714,380 Filed: 10/31/2003  
Examiner: Randal D. Moran Art Unit: 2435  
Docket No.: X-1435 US Conf. No.: 1825

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COMMISSIONER FOR PATENTS  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

AMENDMENT IN RESPONSE TO THIRD FINAL OFFICE ACTION

Dear Sir:

In response to the Final Office Action dated July 9, 2009, please amend the above-referenced application as indicated.

**Amendments to the Claims** are reflected in the listing of claims which begins on page **Error! Bookmark not defined.** of this paper.

**Remarks** begin on page 6 of this paper.